



## VHDL IEEE 1076-2008 is Alive and Thriving

**HENDERSON, Nevada—September 1, 2010** — [Aldec, Inc.](#), a leader in mixed RTL simulation and verification, announces support for the VHDL IEEE 1076™-2008 standard. Aldec has a long history of VHDL support and over 44% of Aldec customers use the language today. Aldec is pleased to announce that VHDL IEEE 1076-2008 is alive and thriving, and is used by a growing percentage of Aldec design and verification customers. Implementation of the new version of VHDL standard is a vital part of the Aldec product offerings and the company pledges continual development of IEEE 1076-2008, and the future editions of the standard.

"VHDL-2008 adds important language enhancements for verification and design engineers. As a VHDL user and trainer, it is exciting to have Aldec announce VHDL-2008 support. Thanks to Aldec for bringing the hard work of the IEEE and Accellera working groups to the VHDL user community," said Jim Lewis, VHDL IEEE Chairperson and Founder of [SynthWorks](#)."

"It was always a challenge to explain little imperfections and annoyances of the language to the trainees and VHDL tool users. Now it is a pleasure to see virtually all those issues eliminated in the new version of the standard, with some extra improvements and clarifications," commented Jaroslaw Kaczynski, Research Engineer, Aldec, Inc.

### About VHDL IEEE 1076-2008

VHDL IEEE 1076-2008 language standard is a powerful, easier to use upgrade from its previous versions. Engineers benefit from a multitude of new functionality, including: PSL incorporation (properties and assertions support), IP protection (encrypted files compilation), VHPI, fixed and floating point packages, generics packages, new types (*integer\_vector* and *boolean\_vector*, etc.), *process (all)* for combinatorial logic, simplified conditional and case statements, extended assignments, new and enhanced operators, extended bit string literals, enhanced port maps, context declarations and clauses. VHDL IEEE 1076-2008 is the biggest VHDL language standard change since the VHDL IEEE 1076-1993 standard.

### Availability

Aldec has included support for VHDL-2008 in both [Active-HDL](#)™ and [Riviera-PRO](#)™ at no additional cost to customers with a valid maintenance contract and with a VHDL or mixed language simulation configuration. Download the latest release of both products from [www.aldec.com](http://www.aldec.com).

### About Aldec

Aldec, Inc., is an industry-leader in Electronic Design Verification and offers a patented technology suite including: RTL Design, RTL Simulators, Hardware-Assisted Verification, Design Rule Checking, IP Cores, DO-254 Functional Verification and Military/Aerospace solutions.

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